Pockels Cells Selection Guide





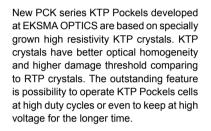
Pockels Cells

PCK

KTP POCKELS CELLS



- More than twice smaller HV requirement comparing to double BBO Pockels cells
- Operates at high duty cycles
- Very low piezo-electric resonances
- Standard available apertures: 4×4, 6×6 and 8×8 mm



Ø2.20

SPECIFICATIONS

M2 ↓

Model	PCK4	PCK4-O	PCK6	PCK6-O	PCK8-O
Clear aperture diameter, mm	3	3.5		5.5	
Crystal size (W×H×L), mm	4×4	4×4×10		6×6×10	
Quantity of crystals			2		
Half-wave voltage (@ 1064 nm), kV DC	<	<1.8		<2.5	
Capacitance, pF		4		<6	
Optical transmission, %			> 98		
Contrast ratio			>1:500		
Cell size, mm	Ø25.4×42.2	25×11.1×7.5	Ø25.4×42.2	25×13.8×10.6	25×16.6×13.4

<u>2 x M2∓3</u>,

7 50

РСК6-О

Ø2.20

APPLICATIONS

rate lasers

Q-switching for high repetition

Pulse picking of high repetition

rate lasers 1 kHz – 1 MHz



For drawings of other standard KTP Pockels Cells please visit www.eksmaoptics.com

RELATED PRODUCTS





DQ High Repetition Rate Pockels Cell Driver for Q-Switching See page 3.13

PCK4-O

DPD Cavity Dumping & Pulse Picking Pockels Cell Drivers See page 3.6

<u>2 x M2∓3</u>,



PCK8-O

ø2.20

POCKELS CELLS

PC • **D**-compact

KD*P POCKELS CELLS



D-compact/12

- Economically priced
- Compact size
- Low absorption
- Transmission from 400 nm to 1100 nm

APPLICATIONS

- Q-switching of the laser cavity
- Cavity Dumping







PC12SR

PC10S

Pockels cells are used to change the polarization state of light passing through it when a voltage is applied to the electrodes of electro-optic crystals such as KD*P. When used in conjunction with polarizer, Pockels cells can be used as fast optical switches.

Typical applications include Q-switching of the laser cavity, laser cavity dumping and coupling light into and from regenerative amplifiers. KD*P based Pockels cells are routinely used for Q-switching applications from the 400 nm to about 1.1 µm. Most of commercial flashlamp pumped Nd:YAG lasers and low repetition rate DPSS Nd:YAG lasers are equipped with KD*P based Pockels cell for laser cavity Q-switching. Electro-optical KD*P crystals have high laser power resistant dielectric AR coatings. Additionally PC12SR and D-compact series Pockels cells have AR coated windows for improved lifetime and protection in less user friendly environment.

All crystals are antireflection coated Damage

12

39

HV input

10

25.4

threshold >5 J/cm² for 10 ns pulses at 1064 nm.

HV pin recept

Outline drawing of D-compact/12

Outline drawing of D-mini/9

Ø25.4

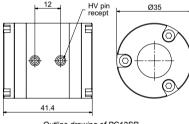
Ø19

22

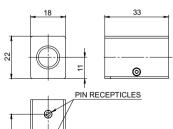
Model	PC5S	PC5D	PC10S	PC12SR	D-compact/9	D-compact/12	D-mini/8	D-mini/9
Clear aperture, mm	4.5×4.5	4.5×4.5	9.5×9.5	Ø11	Ø8	Ø11	Ø7	Ø8
Crystal size, (W×H×L) mm	5×5×16	5×5×16	10×10×25	Ø12×24	Ø9×20	Ø12×24	Ø8×12	Ø9×20
Quantity of crystals	1	2	1	1	1	1	1	1
λ/2 voltage, kV DC	<6.5 ¹⁾	<3.4 ¹⁾	<6.8 ¹⁾	<6.8 ¹⁾	<6.8 1)	<6.8 ¹⁾	<5 ²⁾	<6.8 ¹⁾
Capacitance, pF	1.5	3	4	6	6	6	3	6
Optical transmision, %					>97			
Contrast ratio 3)	> 1:2000	>1:1000	>1:2000	>1:2000	>1:2000	>1:2000	>1:2000	> 1:2000
Cell size, mm	18×14×25	23×16×52	22×18×33	Ø35×41.4	Ø25.4×35	Ø25.4×39	Ø19×19	Ø19×25.4

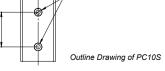
¹⁾ At 1064 nm. ²⁾ At 800 nm.

3) Measured by crossed polarizers method.

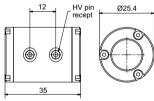


Outline drawing of PC12SR

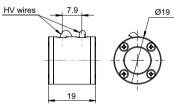




Specifications are subject to change without advance notice



Outline drawing of D-compact/9



Outline drawing of D-mini/8

RELATED PRODUCTS



DQF Pockels Cell Driver for Q-Switching for Flashlamp Pumped Lasers See page 3.14



POCKELS CELLS

POCKELS CELLS

POCKELS CELLS DRIVERS & HIGH VOLTAGE SUPPLIES

PCB

BBO POCKELS CELLS

PCB3D-C PCB3D

- Minimal piezoelectric ringing
- Low absorption
- Broad trasmission range from 200 nm to 2000 nm
- Compact size

APPLICATIONS

- High repetition rate DPSS Q-switch
- High repetition rate regenerative amplifier control
- Cavity dumping
- Beam chopper

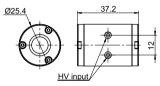
Pockels cell are used to change the polarization state of light passing through it when a voltage is applied to the electrodes of electro-optic crystals such as BBO. When used in conjunction with polarizer, Pockels cells can be used as fast optical switches. Typical applications include Q-switching of the laser cavity, laser cavity dumping and coupling light into and from regenerative amplifiers.

BBO based Pockels cells can be useful at wavelengths from the UV to more than 2 µm. Low piezoelectric ringing makes these Pockels cells attractive for the control of high-power and high pulse repetition rate lasers. Fast switching electronic drivers properly matched to the cell are

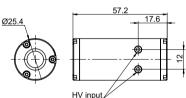
available for Q-switching, cavity dumping and other applications.

Pockels cells of PCB series are transverse field devices. Low electro-optical coefficient of BBO results in high operating voltages. The quarter-wave voltage is proportional to the ratio of electrode spacing and crystal length. As a result, smaller aperture devices have lower quarter-wave, however even for 2.5 mm aperture devices the quarter-wave voltage is as high as 4 kV @ 1064 nm.

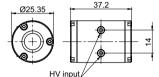
Double crystal design is employed to reduce required voltages and to allow operation in half-wave mode with fast switching times.



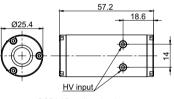
PCB3S, PCB4S outline drawing



PCB3D, PCB4D outline drawing



PCB6.3S outline drawing



PCB6.3D outline drawing

SPECIFICATIONS

Model	PCB3S	PCB3D	PCB4S	PCB4D	PCB6.3S	PCB6.3D	PCB8D
Clear aperture diameter, mm	2	2.5	3	.5	5	.8	7
Crystal size (W×H×L), mm	3×3	3×20	4×4	×20	6.3×6	5.3×20	8×8×20
Quantity of crystals	1	2	1	2	1	2	2
λ/4 voltage (@ 1064 nm), kV DC	<3.5	<1.8	<4.6	<2.3	<7.5	<3.8	<4.6
Capacitance, pF	4	6	3	6	6	<8	<8
Optical transmission, %	> 98	>98	>98	>97	>98	>98	>98
Contrast ratio 1)	>1:1000	>1:500	>1:1000	>1:500	>1:1000	>1:500	>1:500
Dimensions, mm	Ø25.4×37.2	Ø25.4×57.2	Ø25.4×37.2	Ø25.4×57.2	Ø25.4×42.2	Ø25.4×57.2	Ø35×64

1) Measured by crossed polarizers method.

All crystals are coated AR/AR@1064 nm.

Other antireflection coatings are available on request. Damage threshold >5 J/cm² for 10 ns pulses at 1064 nm.

Specifications are subject to changes without advance notice.

RELATED PRODUCTS

Mounting Stage for Pockels Cells of Ø25.4 mm See page 3.5



DQ High Repetition Rate Pockels Cell Driver for Q-Switching See page 3.13

DPD Cavity Dumping & Pulse Picking Pockels Cell Drivers See page 3.6



PULSE PICKING & Q-SWITCHING

PCH1

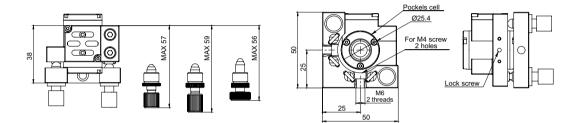
MOUNTING STAGES FOR POCKELS CELLS OF Ø25.4 mm

PCH1 series mounting stage for Pockels cells of 25.4 mm of diameter.

SPECIFICATIONS

Adjusting angle allong X and Y axis	8°
Rotation angle along Z-axis for D-Compact series Pockels cells	22°
Rotating angle along Z-axis for BBO Pockels cells	12°

Catalogue Number	Amount of Adjustment Screws	Adjustment Screw Thread	Screw Sensitivity, µm	Price, EUR
PCH1-2-0.5	2	M6x0.5	1.5	230
PCH1-2-0.35	2	M6x0.35	1.3	265
PCH1-2-0.25	2	M6x0.25	0.5	292
PCH1-3-0.5	3	M6x0.5	1.5	250
PCH1-3-0.35	3	M6x0.35	1.3	306
PCH1-3-0.25	3	M6x0.25	0.5	349



PM1

MOUNTING STAGE FOR POCKELS CELLS OF Ø25.4 mm



SPECIFICATIONS					
Model	PM1				
Adjusting angles, tilt and tip	±3.5°				
Rotation along Z-axis	180°				
Adjustment screw thread	M6×0.25				
Screw sensitivity	0.5 µm				

Catalogue Number	Price, EUF
PM1	250

Other side

HPR-35

Ø35

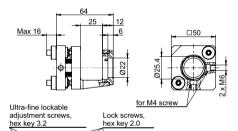
9

20

50

M6×0.5

1.5 µm





Suitable for pockels cells housing diameters, mm

Adjusting angle along X and Y axis, deg

Rotating angle along Z-axis, deg

Adjustment screw thread

Screw sensitivity

Beam high above breadboard, mm

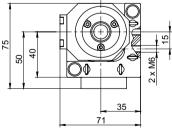
SPECIFICATIONS

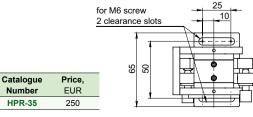
Model

MOUNTING STAGE FOR POCKELS CELLS OF Ø35 mm

HPR series mounting stage for pockels cell is used for housing of Pockells cells in diameter of 35 mm. Stages with fine and

ultrafine screws are available upon request.







Pockels Cells Drivers

DPS/DPD Series DP-SP Series

CAVITY DUMPING & PULSE PICKING POCKELS CELL DRIVERS





Encased version of DPS/DPD series Pockels cell driver

- Fast HV rise/fall time <4 8 ns</p>
- HV pulse amplitude up to 3.6 kV
- Pulse repetition rate up to 3.5 MHz
- Output pulse jitter <50 ps if trigger pulse rise time <0.5 ns

DPD series Pockels cell drivers are designed for wide range of applications and operating modes. Repetition rate can be up to 500 kHz for standard range of drivers, up to 1 MHz enhanced and up to 3.5 MHz high rate. Standard range of possible pulse durations is from 100 ns to 5 µs. It can be extended to infinity using pulse regeneration technique.

Different versions of the drivers are built as variations of voltage, repetition rate and rise/fall time. These three parameters are mutually dependent: higher voltage means longer rise/fall time, and higher repetition rate is limited by voltage.

Connection diagram can be PUSH-PULL configuration using stand-alone driver, as well as FULL BRIDGE using two drivers for one Pockels cell. FULL BRIDGE configuration gives such advantages as repetition rate doubling to reach up to 7 MHz rate, pulse duration shortening down to zero or voltage doubling on pockels cell.

Most of the DPS/DPD series units are available in two versions: "open frame" which is ideal for OEM manufacturers incorporating drivers in their own systems or encased in aluminum housings. Encasing of Pockels cell driver in aluminum housing solves two problems: shields both humans and electronics from high voltage impact from operating Pockels cell driver, and protects driver itself from potentially harmful external contact - ensuring safe operation and driver longevity. The housed option is especially handy for researchers and custom product manufacturers who use these drivers during their own systems build-up. DP-SP modification has possibility to shorten output pulse duration down to 15 ns. Following they can be used for single pulse selection in pulse picker applications for higher repetition rate lasers.

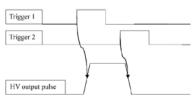


Fig. 1. Control timing charts for two-pulses controlled drivers

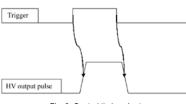
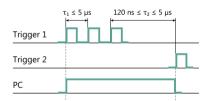


Fig. 2. Control timing charts for single pulse controlled drivers

PULSE REGENERATION TECHNIQUE

Gives possibility to operate push-pull drivers very long output pulse, and full bridge drivers get very low repetition rate. Pulse regeneration does not work with bipolar and short-pulse driver.



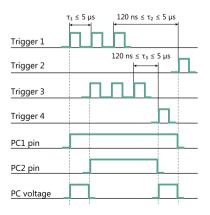


Fig 3. Principle and requirements for long output pulse forming – control way using pulse regeneration. Top diagram is related to DPS/DPD series, bottom for 2DPS/2DPD series (Full-Bridge) drivers.

Pockels cell driver selection chart

This chart represents a summary of the DPD/DPS series of Pockel cells drivers. Horizontal axis depicts repetition rate and vertical axis – operating voltage.

DPS/DPD & DP-SP SERIES DRIVER'S SELECTION CHART

kHz	50	250	300	350	400	500	600	1000	2000	kV
DPS-					400-1.5					1.5
DPD-					400-1.5	-				1.5
DP-SP-									2000-1.6-AI	1.6
2DPS-							600-1.8	1000-1.8		4.0
2DPD-								1000-1.8-AI		1.8
DPS-								1000-2.4-AI		2.4
DPD-		250-2.5					600-2.5			
		250-2.5-AI					600-2.5-AI			2.5
		250-2.5								2.5
		250-2.5-AI								
						500-2.6				2.6
						500-2.6-AI				2.0
								1000-2.9-AI		2.9
Regural drivers	50-3.6	250-3.6								
	50-3.6	250-3.6-AI								3.6
Bipolar drivers		250-3.6								3.0
		250-3.6-AI								
Short-pulse drivers				350-4-AI						4.0
			300-4.6-AI							4.6
Full-bridge drivers		250-5.2-AI								5.2

GENERAL SPECIFICATIONS FOR DPS/DPD AND DP-SP SERIES DRIVERS ¹⁾

Driver model	DPS/DPD series	DP-SP series		
Maximal HV rated voltage (for testing only)	3.8 kV			
Maximal HV operating voltage	<90 % from rated	voltage		
HV pulse rise time	< 4 – 8 ns <i>(Fi</i> g	ı. 7)		
HV pulse fall time	< 4 – 8 ns <i>(Fi</i> g	1. 8)		
HV pulse duration for single driver	100 – 5000 ns	15 – 5000 ns		
Max HV pulse duration for full-bridge configuration	0 - 5000 ns	N/A		
Maximal HV pulse repetition rate	3.5 MHz	600 kHz		
External triggering pulse duration requirement	>100 ns	>10 ns		
External triggering pulse amplitude requirement	3.5 – 5 V (50 Ω load)			
External triggering pulse rise & fall time	< 20 ns	< 5 ns		
Maximal length of leads to Pockels cell	10 cm			
Control diagram options:				
 – single triggering pulse control 	Fig. 2			
 two trigger pulses control 	Fig. 1			
HV pulse delay	45 ns	30 ns		
External powering requirements: 2)				
 high voltage supply 	depends on modi	fication		
- low voltage DC supply	24 ⁺¹ ₋₁₀ , <150 mA 12±0.5 V, <150 mA on request	24±1 V, <150 mA		

 Specifications are given for Pockels cell with capacity <6 pF. Not all combinations of parameters can be possible at the same time. Specifications are subject to changes without advance notice.

²⁾ Driver needs to be mounted on the heatsink (excluding water cooled versions). Heat sink temperature needs to be lower than 35 °C (95 °F) in all regimes of operation.

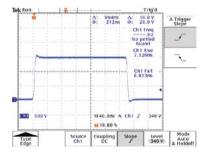


Fig. 4. Typical output pulse shape

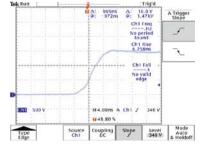


Fig. 5. Typical rising front of output pulse in detail

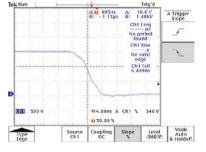


Fig. 6. Typical falling front of output pulse in detail

POCKELS CELLS

OEM version

CONFIGURATION SAMPLES OF OEM VERSION OF DPS/DPD SERIES DRIVERS

Catalogue Number of Driver	DPS-50-3.6 DPD-50-3.6	DPS-400-1.5 DPD-400-1.5	DPS-250-3.6 DPD-250-3.6	DPS-250-2.5 DPD-250-2.5	DPS-500-2.6 DPD-500-2.6	DPS-1000-1.8 DPD-1000-1.8
Maximal HV rated voltage	3.8 kV	1.6 kV	3.7 kV	2.6 kV	2.7 kV	2 kV
Maximal HV operating voltage	3.6 kV	1.5 kV	3.6 kV	2.5 kV	2.6 kV	1.8 kV
Maximal HV repetition rate	50 kHz	400 kHz	250 kHz	250 kHz	500 kHz	1000 kHz
Pulse duration			100 – 5	5000 ns		
HV pulse rise time, typical	<7 ns	<5.5 ns	<7 ns	<6 ns	<6.5 ns	<6 ns
HV pulse fall time, typical	<7 ns	<5.5 ns	<7 ns	<6 ns	<6.5 ns	<6 ns
Output polarity			pos	itive		
HV power consumption	<20 W	<20 W	<75 W	<40 W	<90 W	<80 W
12V/24V power consumption	1 W	5.5 W	4 W	4.5 W	6 W	9 W
Dimensions	see Fig. 8			see Fig. 7		
Cooling	conductive			conductive or water		

DPS in code indicates that driver is controlled by 1 sync pulse, DPD in code indicates that driver is controlled by 2 sync pulses.

HV output voltage to Pockels cell is equal to HV power supply voltage.

Heat sink temperature needs to be lower than 35 °C (95 °F) in all regimes of operation.



OEM version of DPS/DPD series driver

with general purpose pad

OEM version of DPS/DPD series driver with conductive pad. Suitable for repetition rate up to 50 kHz

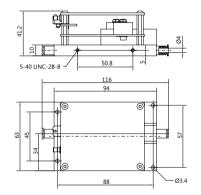


Fig. 7. Outline drawing of DPS series driver with general purpose pad

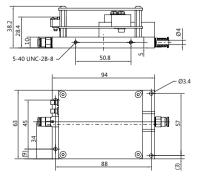


Fig. 7a. Outline drawing of DPD series driver with general purpose pad

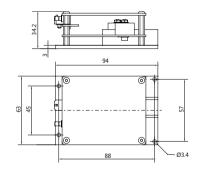


Fig. 8. Outline drawing of DPS series drivers with conductive pad

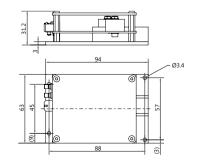


Fig. 8a. Outline drawing of DPD series drivers with conductive pad

EKSMA

OPTICS

ENCASED Short-pulse version

Specific features of DP-SP drivers

- Short circuit protection at driver output
- Driver pad overheat sensor stops operation when overheated
- Overheat optocoupled output signal
- Switchable single pulse and two
 pulses control operation modes
- LED for error indication (overheat and short circuit)

CONFIGURATION EXAMPLES OF ENCASED VERSION OF DP-SP SERIES SHORT-PULSE DRIVERS

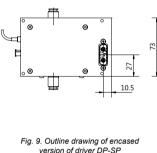
Catalogue Number of Driver	DP-SP-250-3.6-AI	DP-SP-250-2.5-AI	DP-SP-600-2.5-AI
Maximal HV rated voltage	3.7 kV	2.6 kV	2.6 kV
Max / min HV operating voltage	3.6 kV / 1.8 kV	2.5 kV / 1.25 kV	2.5 kV / 1.25 kV
Maximal HV repetition rate	250 kHz	250 kHz	600 kHz
Pulse duration	15-1250 ns	15-1000 ns	15-400 ns
HV pulse rise time, typical	<7 ns	<6 ns	<6.5 ns
HV pulse fall time, typical	<7 ns	<6 ns	<6.5 ns
Output polarity		positive	
HV power consumption	<75 W	<40 W	<100 W
12V/24V power consumption	4 W	4.5 W	7 W
Dimensions		see Fig. 9	
Cooling		water	

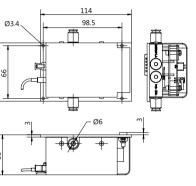
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Heat sink temperature needs to be lower than 35 °C (95 °F) in all regimes of operation. Please specify working voltage and required tuning range by ordering. HV output voltage to Pockels cell is equal to HV power supply voltage.









OEM Short-pulse version

CONFIGURATION EXAMPLES OF OEM VERSION OF DP-SP SERIES SHORT-PULSE DRIVERS

Catalogue Number of Driver	DP-SP-50-3.6	DP-SP-400-1.5	DP-SP-250-3.6	DP-SP-250-2.5	DP-SP-600-2.5	DP-SP-600-1.8
Maximal HV rated voltage	3.8 kV	1.6 kV	3.7 kV	2.6 kV	2.6 kV	2 kV
Max / min HV operating voltage	3.6 kV / 1.8 kV	1.5 kV / 0.75 kV	3.6 kV / 1.8 kV	2.5 kV / 1.25 kV	2.5 kV / 1.25 kV	1.8 kV / 0.9 kV
Maximal HV repetition rate	50 kHz	400 kHz	250 kHz	250 kHz	600 kHz	600 kHz
Pulse duration	15-5000 ns	15-620 ns	15–1250 ns	15-1000 ns	15-400 ns	15-400 ns
HV pulse rise time, typical	<7 ns	<5.5 ns	<7 ns	<6 ns	<6.5 ns	<6 ns
HV pulse fall time, typical	<7 ns	<5.5 ns	<7 ns	<6 ns	<6.5 ns	<6 ns
Output polarity			pos	sitive		
HV power consumption	<20 W	<20 W	<75 W	<40 W	<100 W	<35 W
12V / 24V power consumption	1 W	5.5 W	4 W	4.5 W	7 W	7 W
Dimensions	see Fig. 10			see Fig. 11		
Cooling	conductive			conductive or water		

Driver needs to be mounted on the heatsink (excluding water cooled versions).

Heat sink temperature needs to be lower than 35 $^{\circ}$ C (95 $^{\circ}$ F) in all regimes of operation. Please specify working voltage and required tuning range by ordering.

HV output voltage to Pockels cell is equal to HV power supply voltage.



OEM version of DP-SP series driver with general purpose pad

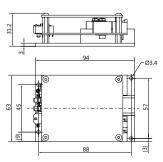


Fig. 10. Outline drawing of DP-SP series drivers with conductive pad

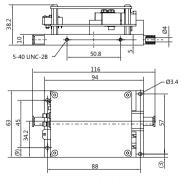


Fig. 11. Outline drawing of DP-SP series driver with general purpose pad

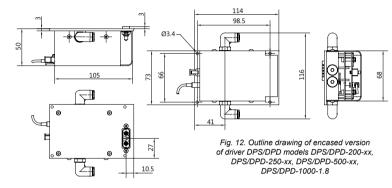
ENCASED version (unipolar drivers)

CONFIGURATION SAMPLES OF ENCASED VERSION OF DPS/DPD SERIES UNIPOLAR DRIVERS

Catalogue Number of Driver	DPS-250-3.6-AI DPD-250-3.6-AI			DPS-1000-1.8-AI DPD-1000-1.8-AI		
Maximal HV rated voltage	3.7 kV	2.6 kV	2.7 kV	2 kV		
Maximal HV operating voltage	3.6 kV	2.5 kV	2.6 kV	1.8 kV		
Maximal HV repetition rate	250 kHz	250 kHz	500 kHz	1000 kHz		
Pulse duration		100 – 5000 ns				
HV pulse rise time, typical	<7 ns	<6 ns	<6.5 ns	<6 ns		
HV pulse fall time, typical	<7 ns	<6 ns	<6.5 ns	<6 ns		
Output polarity		positive				
HV power consumption	<75 W	<40 W	<90 W	<80 W		
12V/24V power consumption	4 W	4.5 W	6 W	9 W		
Dimensions		see Fig. 12				
Cooling	water					



Encased version of driver DPS/DPD models DPS/DPD-200-xx, DPS/DPD-250-xx, DPS/DPD-500-xx, DPS/DPD-1000-1.8



ENCASED version (bipolar drivers)

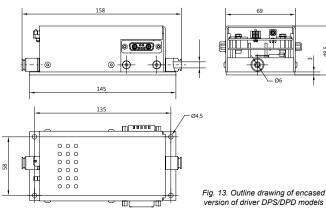
CONFIGURATION SAMPLES OF ENCASED VERSION OF DPS/DPD SERIES BIPOLAR DRIVERS

Catalogue Number of Driver	DPS-250-5.2-AI DPD-250-5.2-AI	DPS-300-4.6-AI DPD-300-4.6-AI	DPS-350-4-AI DPD-350-4-AI	DPS-1000-2.9-AI DPD-1000-2.9-AI			
Maximal HV rated voltage	5.3 kV	4.8 kV	4.2 kV	3.0 kV			
Maximal HV operating voltage	5.2 kV	4.6 kV	4.0 kV	2.9 kV			
Maximal HV repetition rate	250 kHz	300 kHz	350 kHz	1000 kHz			
Pulse duration		100 – 5000 ns					
HV pulse rise time, typical	<8.5 ns	<8 ns	<7.5 ns	<7.5 ns			
HV pulse fall time, typical	<8.5 ns	<8 ns	<7.5 ns	<7.5 ns			
Output polarity	bipolar						
HV power consumption	<100 W	<100 W	<100 W	<120 W			
12V/24V power consumption	9 W						
Dimensions	see Fig. 13						
Cooling	conductive or water						

Driver needs to be mounted on the heatsink (excluding water cooled versions). Heat sink temperature needs to be lower than 35 °C (95 °F) in all regimes of operation.

HV output voltage to Pockels cell is equal to HV power supply voltage i.e. sum of positive and negative HV values. Please specify working voltage and required tuning range by ordering.

Encased version of driver DPS/DPD models. Water cooled version * Bipolar HV power supply HV2x60Wm is specifically designed for these drivers.



MEKSMA

OPTICS

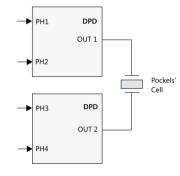
FULL-BRIDGE version

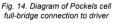
CONFIGURATION EXAMPLES OF FULL-BRIDGE CONFIGURATION DRIVERS

Catalogue Number of Full-Bridge Driver	2DPS-1000-2.4-AI 2DPD-1000-2.4-AI	2DPS-2000-1.6-AI 2DPD-2000-1.6-AI	
Base driver	DPS-500-2.5 DPD-500-2.5	DPS-1000-1.8 DPD-1000-1.8	
Maximal HV operating voltage	2.4 kV	1.6 kV	
Maximal HV repetition rate with frequency doubling ¹⁾	1000 kHz	2000 kHz	
HV pulse duration range for OUT1	100 – 5	5000 ns	
HV pulse duration range for OUT2	100 – 5	5000 ns	
HV pulse duration range to Pockel's cell	0 – 5000 ns		
HV pulse rise time	<6.5 ns	<6 ns	
HV pulse fall time	<6.5 ns	<6 ns	
Maximal capacitance of Pockels cell	<6	pF	
HV power consumption	<160) W ²⁾	
Case	see Fig. 16		
Cooling	wa	ater	
⁾ Full-bridge drivers are generally designed for operation at high	Heat sink temperature needs	to be lower than 35 °C (95 °F) in all regim	

¹⁾ Full-bridge drivers are generally designed for operation at high repetition rate. However, low repetition rate pulsing can be achieved using pulse regeneration technique.

²⁾ Two HV power supplies PS-80 are suggested.







External view of full-bridge driver

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of operation. HV output voltage pulse to Pockels cell appears as difference of pulses OUT1 and OUT2 and is lower as HV power supply voltage.

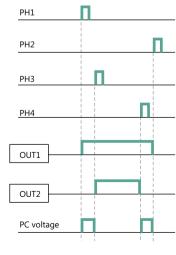
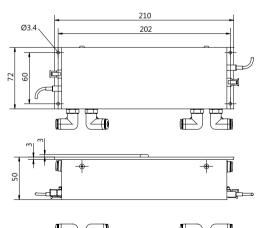


Fig. 15. Principle of 4-phase control of full-bridge driver configuration



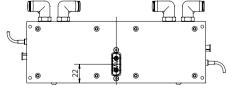


Fig. 16. Outline drawing of case for full-bridge drivers

DPB

HIGH VOLTAGE POCKELS CELL DRIVER

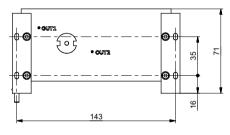
SPECIFICATIONS

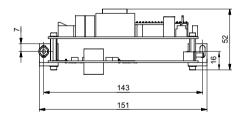
DPB series driver mounted with HV power supply PS-5

- HV pulse amplitude up to 5.6 kV
- HV pulse duration down to 15 ns
- HV pulse amplitude doubling layout
- Repetition rate up to 10 kHz
- Easy integration with HV power supply
- Switchable one/two trigger pulses control model

Catalogue Number	DPB-10-4.2	DPB-5-5.6	
Maximum high voltage (HV) pulse amplitude to cell	4.2 kV	5.6 kV	
Output polarity	bipolar		
HV pulse rise time, typical	7 ns	8 ns	
HV pulse fall time, typical	7 ns	8 ns	
HV pulse duration	30 – 3	000 ns	
Maximal HV pulse repetition rate	10 kHz	5 kHz	
External triggering pulse amplitude requirement	3.5 – 5 V (50 Ω load)	
External triggering pulse rise & fall time	< 5	ns	
HV pulse delay	30 ns		
External powering requirements:			
HV power supply*	≤ 2.1 kV, 5W	≤ 2.8 kV, 5 W	
low voltage DC supply	,	A or 12 V, 220 mA sh current)	
Dimensions (L × W × H):			
driver board	135 × 65 × 25 mm		
driver board mounted with PS-5 power supply	135 × 65	× 55 mm	

* Typical voltage control limits for PS-5 are 1.8 to 2.8 kV. We accept other limits on your request.





Outline drawing

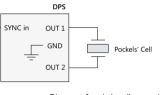
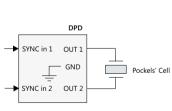




Diagram of pockels cell connection to driver and timing charts of driver controlled by 1 sync pulse DPB-10-4.2S or DPB-5-5.6S



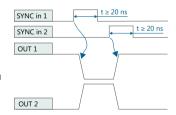


Diagram of pockels cell connection to driver and timing charts of driver controlled by 2 sync pulses DPB-10-4.2D or DPB-5-5.6D

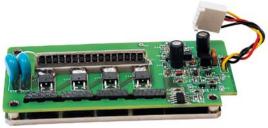
ORDERING INFORMATION

Catalogue Number	Description
DPB-5-5.6S	Controlled by 1 sync pulse; Maximal HV pulse amplitude to cell 5.6 kV; Maximal HV pulse repetition rate 5 kHz
DPB-5-5.6D	Controlled by 2 sync pulses; Maximal HV pulse amplitude to cell 5.6 kV; Maximal HV pulse repetition rate 5 kHz
DPB-10-4.2S	Controlled by 1 sync pulse; Maximal HV pulse amplitude to cell 4.2 kV; Maximal HV pulse repetition rate 10 kHz
DPB-10-4.2D	Controlled by 2 sync pulses; Maximal HV pulse amplitude to cell 4.2 kV; Maximal HV pulse repetition rate 10 kHz

EKSMA

OPTICS

HIGH REPETITION RATE POCKELS CELLS DRIVER FOR Q-SWITCHING



- Pulse repetition rate up to 100 kHz
- Fast HV rise time <7 ns for 4 kV pulse</p>
- HV pulse amplitude up to 4 kV

DQ series high repetition rate Pockels cell driver has been designed for use in mode-locked lasers for cavity dumping or for cavity Q-switching of solid-state nanosecond lasers. Fast HV (less than 7 ns) edge ensures excellent pre- and post-pulse contrast.

Ability to operate at high pulse repetition rates makes this driver perfect fit for most of diode-pumped nanosecond lasers. For pulse repetition rates up to 10 kHz heatsink is not required. For high repetition rates the driver should be attached to the heatsink with thermal resistance of at least 0.4 °C/W for room temperature (25 °C) operation.

The driver should be mounted into dielectric box (not provided) providing electrical insulation. Low voltage power supply is required to internal triggering circuit, while tuning of HV power supply voltage.

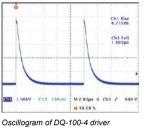


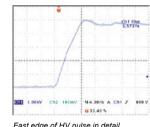
Catalogue Number	DQ-100-4
Maximum high voltage (HV) pulse amplitude	4.0 kV
Polarity	Positive
HV pulse rise time	< 7 ns
HV pulse fall time	~2 µs 1)
HV pulse duration	180 ns 1)
Maximum HV pulse repetition rate	100 kHz
HV pulse jitter	< 0.5 ns
External triggering pulse duration requirement	100-1000 ns
External triggering pulse amplitude requirement	3-5 V (50 Ω)
External triggering pulse rise & fall time	< 10 ns
HV pulse delay	35-40 ns
External powering requirements:	
high voltage supply	0 - 4.0 kV, 9 mA max 2)
low voltage DC supply	9 - 24 V, 500 mA max 2)
Operating temperature	0-35 °C 3)
Size	104×52×25 mm

1) Typical value.

²⁾ Test conditions 'PRR = 100 kHz C = 6 nF U = 4 kV

³⁾ Heatsink temperature should be bellow 35 °C at 100 kHz pulse repetition rate.





operation

Fast edge of HV pulse in detail



CAVITY DUMPER DRIVER

SPECIFICATIONS

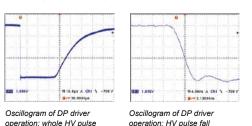
Size

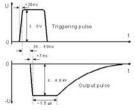
DP has been designed for use in modelocked lasers for cavity dumping or for cavity Q-switching of solid-state nanosecond lasers. Fast HV (less than 7 ns) edge ensures excellent pre- and postpulse contrast.

Catalogue Number	DP-3-4.2
Maximum high voltage (HV) pulse amplitude	4.2 kV
HV pulse fall time	< 7 ns
HV pulse rise time	~ 0.1 ms
HV pulse duration	from 5 to 100 μs $^{1)}$
Maximum HV repetition rate	3 kHz
Jitter	< 0.5 ns
External triggering pulse duration requirement	100 – 1000 ns
External triggering pulse amplitude requirement	3-5 V (50 Ω)
External triggering pulse rise & fall time	< 20 ns
HV pulse delay	35 – 40 ns

External powering requirements: high voltage supply 4.4 kV, 0.2 mA max 24 - 28 V, 50 mA max low voltage DC supply 100 × 50 × 40 mm

¹⁾ According to customers request.





Time diagram of DP cavity dumpina driver

PULSE PICKING & Q-SWITCHING

POCKELS CELLS DRIVERS & HIGH VOLTAGE SUPPLIES

POCKELS CELLS

POCKELS CELLS DRIVERS & HIGH VOLTAGE SUPPLIES

DQF



External view of DQF-0.2-5 driver



External view of DQF-0.1-8 driver

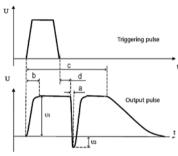


Fig. 1. Time diagram of DQF driver

POCKELS CELLS DRIVER FOR Q-SWITCHING FOR FLASHLAMP PUMPED LASERS

DQF drivers are designed for Q-switching of nanosecond lasers without use of phase retardation plate. High voltage is applied to Pockels cell in order to inhibit oscillation. Pockels cell is opened by negative polarity pulse allowing laser to radiate.

Drivers DQF-0.1-8D and DQF-0.1-8B are integrated with ± 4 kV HV power supply. Voltage control is done using CAN inter-

face. We suggest CAN-USB converter with CAN browser software for Windows[®] operating system. CAN browser can be kept disconnected after proper voltage value was set.

Drivers DQF-0.2-5D and DQF-0.1-8D are designed for operation with DKDP crystals, drivers DQF-0.2-5B and DQF-0.1-8B – for BBO.

SPECIFICATIONS

Catalogue Number	DQF-0.2-5D	DQF-0.2-5B	DQF-0.1-8D	DQF-0.1-8B	
Maximum high voltage to cell (HV) pulse amplitude (U1 + U2)	5 kV	4 kV	8 kV	7.6 kV	
U1 value (Fig. 1)	equal to HV powering voltage				
U2 value (Fig. 1)	equal to 0.25×U1	0 V	equal to 0.25×U1	0 V	
HV pulse fall time (a)	< 1	5 ns	< 12	2 ns	
HV pulse rise time, typical (b)	60	60 µs) µs	
HV pulse duration, typical (c)	300 μs (1200 μs optionally)		650 µs		
HV pulse repetition rate	≤ 25	0 Hz	≤ 100 Hz		
HV pulse delay (d)	40 ns		25 ns		
External triggering pulse duration	100 – 1	100 – 1200 µs		650 µs	
External triggering pulse amplitude	3 – 5 V	3 – 5 V (50 Ω)		3.5 – 5 V (50 Ω)	
External triggering pulse rise & fall time	< 20 ns		< 20 ns		
Board dimensions	92 × 70 × 22 mm 1)		92 × 70 × 27 mm 1)		
Mounting holes location for M3 studs	84 × 62 mm		84 × 62 mm		
External powering requirements:					
DC supply	12 – 24 V, r	12 – 24 V, max 200 mA		12 V, max 15 mA	
HV supply	4 kV, 1 mA		n	/a	

¹⁾ Keep safety distance at least 5 mm from any side of board or any component to surrounding conductive parts.

PS

HIGH VOLTAGE POWER SUPPLY



PS-40 power supply



PS-80 power supply

PS-120, PS2-60 power supply

Output ripple is 0.5% typically at maximal power for all models of HV power supplies. External filter can be used to reduce that value if certain application requires smoother output. All Pockets cell drivers have integrated ripple filter and usually do not require additional one. Contact us for suggestions if your specific application needs lower ripple voltage. Power supply PS series is designed for powering pockels cell driver with appropriate power and voltage requirement. Due to its performance and reliable design, PS is good choice for OEM customers.

Table below shows general specifications of HV power supplies. Particularly "Maximal output high voltage" row gives reference of maximal achievable voltage for particular model limited by design. For specific driver this limitation is different. E.g. for the driver DPS-1000-1.8 voltage control limits are made from 0.8 kV to 1.8 kV typically.

SPECIFICATIONS

Catalogue Number	PS-5	PS-40	PS-80	PS-120	PS2-60	
Maximal high voltage	1.8 kV	1.3 kV 1.8 kV	1.8 kV 2.6 kV	1.8 kV 2.6 kV	±1.4 kV	
output options 1)	2.8 kV	2.5 kV 3.6 kV	3.1 kV 3.6 kV	3.1 kV 3.6 kV	±2.6 kV	
	4.0 kV	4.0 kV	4.0 kV 4.4 kV			
Voltage control limits		-	1 kV from maximal valu	e		
Maximal output power at maximal output voltage ²⁾	5 W	40 W	80 W	120 W	2×60 W	
Output voltage control options 3)		CAN interface ⁴), internal trimmer				
External powering	24 V DC, <15 W 5)	24 V DC, <50 W	24 V DC, <90 W	24 V DC, <150 W	24 V DC, <150 W	
Dimensions (L × W × H)	135×70×30 mm	160×70×40 mm	175×70×45 mm	175×110×45 mm	175×110×45 mm	
Mounting holes Ø3.4 mm location	125×35 mm	150×60 mm	165×60 mm	165 × 100 mm	165×100 mm	

¹⁾ Matching to Pockels cell driver voltage requirement is necessary by ordering.

²⁾ Maximal power is proportionally lower by tuning to lower HV output.

3) Needs to be indicated by ordering.

Requires USB-CAN converter for computer control that is sold separately.
 Optionally 12 V DC, <15 W.

PULSE PICKING & Q-SWITCHING

POCKELS CELLS

PS4012

HIGH VOLTAGE POWER SUPPLY FOR POCKELS CELL DRIVERS





- HV power supply and low voltage 24 V DC power supply in one box
- Tailored for use in laboratories
- HV tuning by multi-turn knob 31/2 LCD display for voltage • monitoring
- Various versions for different . power requirement

Desktop HV power supply PS4012 is designed for powering Pockels cell drivers. It features internal HV power supply as well as low voltage 24 V DC power supply in one case.

SPECIFICATIONS

Model	PS4012-05	PS4012-40	PS4012-80	PS4012-120	PS4012-150	PS4012-2×60	
Maximal high voltage	1.8 kV	1.3 kV	1.8 kV	1.8 kV	1.8 kV	±1.4 kV	
output options	2.8 kV	1.8 kV	2.6 kV	2.6 kV	2.6 kV	±2.6 kV	
	4.0 kV	2.5 kV	3.1 kV	3.1 kV	3.1 kV		
		3.6 kV	3.6 kV	3.6 kV	3.6 kV		
			4.0 kV		4.0 kV		
			4.4 kV		4.4 kV		
Maximal output power at maximal output voltage 1)	5 W	40 W	80 W	120 W	150 W	2×60 W	
Voltage control limits		40% U _{max} to U _{max}					
24 V DC remaining power for external needs (including PCD)	190 W	150 W	100 W	60 W	20 W	60 W	
Mains voltage	90 to 264 V AC, 47–63 Hz						
Dimensions (W×L×H)	260 × 280 × 100 mm						
Weight	2.7 kg						

¹⁾ Maximal power is proportionally lower by tuning to lower HV output.

Output ripple is 0.5% typically at maximal power for all models of HV power supplies. External filter can be used to reduce that value if certain application requires smoother output.



Pulse Picking – & Q-Switching

UP1

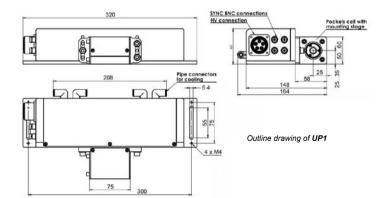
ULTRAFAST PULSE PICKER



pMaster 4.0H - Digital synchronization and delay pulse generator with built-in HV power supply



UP1 – Ultrafast pulse picker



UP1 Pulse picker consists of built-in drivers in full bridge configuration and a Pockels cell attahed to the unit. In setup with pMaster 4.0H generator, UP1 pulse picker is able to select pulses at up to 1 MHz rate from max 100 MHz repetition rate pulse train.

UP1 comes with a BBO Pockels cell (set for quarter wave voltage) or a KTP Pockels cell (for half wave operation). KTP Pockels cell usage is limited by the average power of the laser beam – up to 2 W and contrast ratio is typically >1:500. While BBO Pockels cells operate at much higher power levels and feature higher contrast ratio – typically >1:1000.

We recommend to use UP1 in a set with digital synchronization and delay pulse generator pMaster 4.0H for synchronization of your laser with Pockels cell driver and triggering of Pockels cell driver used for pulse picking from the train of picosecond or femtosecond pulses. **pMaster 4.0H** has a built-in high voltage power supply and can be used with UP1 pulse picker for pulse picking at up to 1 MHz rate.

Suggested configuration

Ultrafast pulse picking at up to 1 MHz rate



POCKELS CELLS

Pulse Picker's Control Software and Operation Scheme

See page 3.22.

SPECIFICATIONS OF DIGITAL SYNCHRONIZATION AND DELAY PULSE GENERATOR pMASTER 4.0H

Model	pMaster 4.0H		
PROGRAMMABLE TIMING GENERATOR			
Channel modes	Single shot, burst, normal, duty cycle		
Control modes	Internally triggered, externally triggered and external gate		
Delay range	0 to 1000 s		
Delay accuracy	1.5 ns + 0.0001 delay		
Delay resolution	250 ps		
Delay Jitter	<400 ps RMS		
Pulse inhibit delay / output inhibit delay	120 ns / 50 ns		
TRIGGER INPUT MODULE			
Trigger input rate	DC – 5 MHz		
Trigger insertion delay	<180 ns		
Trigger jitter	<800 ps RMS		
Minimal pulse width	2 ns		
Trigger threshold	0.2 – 15 V DC		
Maximum input voltage	60 V Peak		
Input impedance	1.5 k ohms + 40 pF		
Resolution	10 mV		
EXTERNAL CLOCK INPUT MODULE			
External clock input rate	10 MHz – 100 MHz		
Minimal pulse width	100 ps		
Pulse amplitude	1 V rms (min) – 5 V rms (max)		
Input impedance	102 ohms		
PHYSICAL SPECIFICATIONS			
High voltage power supply for PC driver	Built-in		
Dimensions W × D × H	482 × 387 × 88 mm		

SPECIFICATIONS OF PULSE PICKER UNIT

Pulse picker	UP1 - BBO	UP1 - KTP	
Built-in driver	operates at up to 1 MHz rep. rate		
Max laser repetition rate for single pulse picking	100	MHz	
HV power supply	requires 1)		
Operation	quarter wave, λ/4	half-wave, λ/2	
HV pulse duration	0 – 50	00 ns	
HV pulse rise and fall time	<6.5 ns		
Pockels cell contrast ratio, VCR 2)	>1:500		
Pockels cell transmission 3)	>98 % at 1030 nm	>98 % at 1064 nm	
Clear aperture	Ø3.5 mm	Ø5 mm 4)	
Cooling	wa	ter	
Dimensions L × W × H	320 × 164 × 65 mm		

¹⁾ Requires two HV power supplies with max 4 kV output and maximal output power 120 W each.

Optimal HV power supplies are provided in generator pMaster 4.0H.

²⁾ VCR – contrast ratio when voltage is applied to the cell.

³⁾ Other particular laser wavelengths or wavelength ranges are available on request.

⁴⁾ Max clear aperture for KTP Pockels cell can be up to Ø9 mm.

ORDERING INFORMATION

Code	Description
pMaster 4.0H	Pulse synchronization and delay generator, 4 output channels for trigger pulses with built-in High Voltage supply
UP1-BBO-2.5	Ultrafast pulse picker for up to 1 MHz operating rate, BBO clear aperture 2.5 mm, $\lambda/4$ operation at 1030-1064 nm
UP1-BBO-3.5	Ultrafast pulse picker for up to 1 MHz operating rate, BBO clear aperture 3.5 mm, $\lambda/4$ operation at 1030-1064 nm
UP1-KTP-5.5	Ultrafast pulse picker for up to 1 MHz operating rate, KTP clear aperture 5.5 mm, $\lambda/2$ operation at 1030-1064 nm

MP1

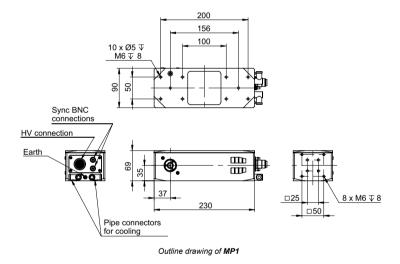
FAST PULSE PICKER



pMaster 4.0H – Digital synchronization and delay pulse generator with built-in HV power supply



MP1 – Fast pulse picker



MP1 pulse picker consists of built-in fast driver and a Pockels cell.

This unit is able to select pulses at up to 600 kHz rate. MP1 requires sync pulses from the laser for the driver control or can be used with **pMaster 4.0H** generator. In setup with pMaster 4.0H generator can be synchronized for single pulse picking from max 30 MHz repetition rate pulse train.

MP1 pulse picker has BBO Pockels cell and is set for quarter wave voltage operation. On special requests BBO cell can be changed to KTP Pockels cell for half wave voltage operation.

Suggested configuration

Ultrafast pulse picking at up to 600 kHz rate



Pulse Picker's Control Software and Operation Scheme

See page 3.22.

POCKELS CELLS

SPECIFICATIONS OF DIGITAL SYNCHRONIZATION AND DELAY PULSE GENERATOR pMASTER 4.0H

Model	pMaster 4.0H		
PROGRAMMABLE TIMING GENERATOR			
Channel modes	Single shot, burst, normal, duty cycle		
Control modes	Internally triggered, externally triggered and external gate		
Delay range	0 to 1000 s		
Delay accuracy	1.5 ns + 0.0001 delay		
Delay resolution	250 ps		
Delay Jitter	<400 ps RMS		
Pulse inhibit delay / output inhibit delay	120 ns / 50 ns		
TRIGGER INPUT MODULE			
Trigger input rate	DC – 5 MHz		
Trigger insertion delay	<180 ns		
Trigger jitter	<800 ps RMS		
Minimal pulse width	2 ns		
Trigger threshold	0.2 – 15 V DC		
Maximum input voltage	60 V Peak		
Input impedance	1.5 k ohms + 40 pF		
Resolution	10 mV		
EXTERNAL CLOCK INPUT MODULE			
External clock input rate	10 MHz – 100 MHz		
Minimal pulse width	100 ps		
Pulse amplitude	1 V rms (min) – 5 V rms (max)		
Input impedance	102 ohms		
PHYSICAL SPECIFICATIONS			
High voltage power supply for PC driver	Built-in		
Dimensions W × D × H	482 × 387 × 88 mm		

SPECIFICATIONS OF PULSE PICKER UNIT

Pulse picker	MP1 - BBO
Built-in driver	operates at up to 600 kHz rep. rate
Max laser repetition rate for single pulse picking	30 MHz
HV power supply	requires 1)
Operation	quarter wave, λ/4
HV pulse duration	15 – 5000 ns
HV pulse rise and fall time	<7 ns
Pockels cell contrast ratio, VCR 2)	>1:500
Pockels cell transmission 3)	>98 % at 1030 nm
Clear aperture	Ø3.5 mm
Cooling	water
Dimensions L × W × H	230 × 90 × 69 mm

¹⁾ Requires one HV power supply with max 4 kV output and maximal output power 120 W each. Optimal HV power supplies are provided in generator pMaster 4.0H.

2) VCR – contrast ratio when voltage is applied to the cell.

³⁾ Other particular laser wavelengths or wavelength ranges are available on request.

ORDERING INFORMATION

Code	Description
pMaster 4.0H	Pulse synchronization and delay generator, 4 output channels for trigger pulses with built-in High Voltage supply
MP1-BBO-3.5	Pulse picker for up to 600 kHz operating rate, BBO clear aperture 3.5 mm, $\lambda/4$ operation at 1030-1064 nm
Minichiller 300-H	Chiller for water cooling of HV drivers (optional)





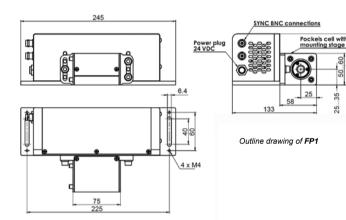




pMaster 4.0 – Digital synchronization and delay pulse generator



FP1 pulse picker

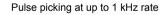


Pulse picker FP1 consists of a built-in driver with a high voltage power supply and a Pockels cell attached to the unit. FP1 pulse picker is able to select pulses at up to 1 kHz rate.

FP1 requires sync pulses from the laser for driver control or it can be used with pMaster 4.0 pulse synchronization and delay generator. In setup with pMaster 4.0 generator FP1 can be synnchronized for single pulse picking from max 20 MHz repetition rate pulse train. Standard FP1 pulse picker has a DKDP Pockels cell and is set for quarter wave voltage operation. On special requests DKDP cell can be changed to BBO or KTP Pockels cell for half wave voltage operation.

For synchronization of your laser with Pockels cell driver and triggering of Pockels cell driver used for pulse picking from the train of picosecond or femtosecond pulses we recommend to use **FP1 Pulse picker in setup** with pMaster 4.0 digital synchronization and delay pulse generator.

Suggested configurations



1 or 2 sync pulses from the laser	24 V DC, 15 W

Pulse picker FP1

pMaster 4.0

Pulse picking at up to 1 kHz rate

Pulse Picker's	Control	Software	and	Operation	Scheme
	001101	Continuito	ana	oporation	Conorno

See page 3.22.

SPECIFICATIONS OF DIGITAL SYNCHRONIZATION AND DELAY PULSE GENERATOR pMASTER 4.0

Model	pMaster 4.0	
PROGRAMMABLE TIMING GENERATOR		
Channel modes	Single shot, burst, normal, duty cycle	
Control modes	Internally triggered, externally triggered and external gate	
Delay range	0 to 1000 s	
Delay accuracy	1.5 ns + 0.0001 delay	
Delay resolution	250 ps	
Delay Jitter	<400 ps RMS	
Pulse inhibit delay / output inhibit delay	120 ns / 50 ns	
TRIGGER INPUT MODULE		
Trigger input rate	DC – 5 MHz	
Trigger insertion delay	<180 ns	
Trigger jitter	<800 ps RMS	
Minimal pulse width	2 ns	
Trigger threshold	0.2 – 15 V DC	
Maximum input voltage	60 V Peak	
Input impedance	1.5 k ohms + 40 pF	
Resolution	10 mV	
EXTERNAL CLOCK INPUT MODULE		
External clock input rate	10 MHz – 100 MHz	
Minimal pulse width	100 ps	
Pulse amplitude	1 V rms (min) – 5 V rms (max)	
Input impedance	102 ohms	
PHYSICAL SPECIFICATIONS		
High voltage power supply for PC driver	-	
Dimensions W × D × H	482 × 283 × 44 mm	

SPECIFICATIONS OF PULSE PICKER UNIT

Pulse picker	FP1 - DKDP
Built-in driver	operates at up to 1 kHz rep. rate
Max laser repetition rate for single pulse picking	20 MHz
HV power supply	built-in 1)
Operation	quarter wave, λ/4
HV pulse duration	15 – 3000 ns
HV pulse rise and fall time	<6.5 ns
Pockels cell contrast ratio, VCR 2)	>1:2000
Pockels cell transmission 3)	>97% at 1064 nm
Clear aperture	Ø11 mm
Cooling	conductive heat sink
Dimensions L × W × H	245 × 133 × 81 mm

¹⁾ Requires only 24 V, 15 W external power supply.

²⁾ VCR – contrast ratio when voltage is applied to the cell.

³⁾ Other particular laser wavelengths or wavelength ranges are available on request.

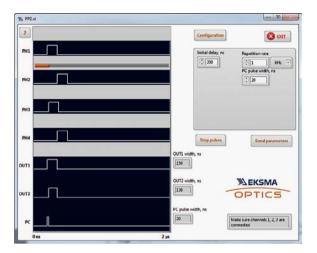
ORDERING INFORMATION

Code	Description
pMaster 4.0	Pulse synchronization and delay generator, 4 output channels for trigger pulses
FP1-DKDP-11	Pulse picker with built in HV supply for up to 10 kHz operating rate, DKDP clear aperture 11 mm, λ /4 operation at 1064 nm

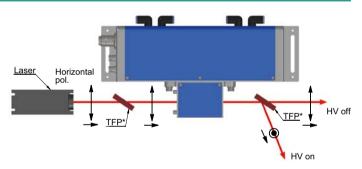
PULSE PICKER'S CONTROL SOFTWARE AND OPERATION SCHEMES

Control software

pMaster features 4 independent programmable channel outputs and communication via USB port with LabView compatible drivers for full control over all parameters.

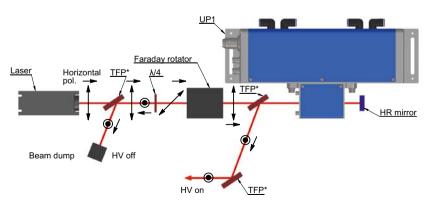


Suggested Half-wave voltage operation scheme



* TFP – Thin film polarizer, for instance our standard products: 420-1258UHT, 420-1256UHT or 420-1248UHT.

Suggested Quater-wave voltage operation scheme



* TFP – Thin film polarizer, for instance our standard products: 420-1258UHT, 420-1256UHT or 420-1248UHT.

OEM DKDP POCKELS CELLS KIT FOR Q-SWITCHING OF FLASH LAMP PUMPED LASERS

Code

QKD-01

QKD-02

OKD-03

 Pulse repetition rate up to 200 Hz

plate

Q-switching without retardation

EKSMA OPTICS offers Pockels cells kit for lamp pumped nanosecond lasers. High voltage through Pockels cell driver DQF is applied to Pockels cell in order to inhibit oscillation. Pockels cell is opened by negative polarity pulse allowing laser to radiate.

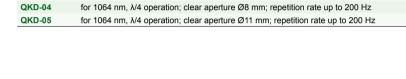
DKDP POCKELS CELLS Q-SWITCHING KIT INCLUDES:

- Low repetition rate Pockels Cell driver see page 3.13
- High voltage power supply for Pockels Cell driver see page 3.14
- DKDP Pockels cells see page 3.3

OEM DKDP POCKELS CELL KITS

U 1 230 m 1 2300 m 1 230

Time diagram of DQF Pockels cell driver

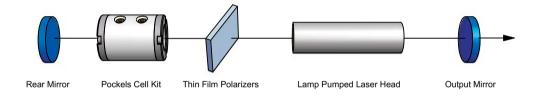


Description

for 1064 nm, λ/4 operation; clear aperture 4.5×4.5 mm; repetition rate up to 200 Hz

for 1064 nm, λ/4 operation; clear aperture 9.5×9.5 mm; repetition rate up to 200 Hz

for 1064 nm, λ/4 operation; clear aperture Ø11 mm; repetition rate up to 200 Hz



OEM BBO POCKELS CELL KIT FOR Q-SWITCHING OF DPSS LASERS

- Pulse repetition rate up to 100 kHz
- Up to 10 kHz no external cooling is required
- Fast HV rise time <7 ns

EKSMA OPTICS offers BBO Pockels cell kit for Q-Switching of high repetition rate nanosecond lasers and mode locked lasers.

BBO POCKELS CELLS Q-SWITCHING KIT INCLUDES:

- High repetition rate Pockels Cell driver see page 3.13
- High voltage power supply for Pockels Cell driver see page 3.14
- BBO Pockels cell see page 3.4

OEM BBO POCKELS CELLS KITS

Code	Description
QKB-01	for 1064 nm, $\lambda\!/4$ operation; clear aperture 2.5 mm; repetition rate up to 100 kHz
QKB-02	for 1064 nm, $\lambda\!/4$ operation; clear aperture 3.5 mm; repetition rate up to 100 kHz

Pockels cells and Drivers

BBO Pockels Cells for 1064 nm and 1020-1040 nm

KD*P Pockels Cells for 694 nm, 1064 nm and broadband 740-840 nm

KTP Pockels Cells for 1064 nm

Drivers and HV Power Supplies

Pulse Picking Solutions

Q-Switching Kits



